# ROHS

# **LEADER TIME SRL**

# **PRODUCT SPECIFICATION**

# 16\*2 Characters COB LCD MODULE MODEL: LT-1602E-605 Ver:1.1

< > > Finally Specification

CUSTOMER'S APPROVAL				
NATURE:	DATE:			
•				

APPROVED	РМ	PD	PREPARED
BY	REVIEWD	REVIEWD	Ву

# Prepared By: LEADER TIME SRL

VIA MONS. PROSDOCIMI, 27 36042 BREGANZE (VI)

• This specification is subject to change without notice. Please contact LT or its representative before designing your product based on this specification.

1/1

# **Revision Status**

Version	Revise Date	Page	Content	Modified By
Ver. 1.0	2018.01.23		First Issued	
Ver. 1.1	2018.05.17	6,19	Change backlight is 5.0V;Change R9 Value and ADD R10 Value to 120R.	

# **Table of Contents**

No. Contents	Page
1. FEATURES	4
2. MECHANICAL SPECIFICATIONS	4
3. ELECTRICAL SPECIFICATIONS	4
4. TERMINAL FUNCTIONS AND BLOCK DIAGRAM	6
5. TIMING CHARACTERISTICS	7
6.COMMAND LIST	10
7.CHARACTER GENERATOR ROM	11
8. QUALITY SPECIFICATIONS	12
9. RELIABILITY	17
10. HANDLING PRECAUTION	18
11. OUTLINE DIMENSION	19

3/19

### 1. Features

The features of LCD are showed as follows

\* Display mode : FSTN/ Transflective/Positive \* Controller IC : UCI7066-03(or equivalent)

\* Display format : 16X2Characters \* Interface Input Data : 6800-8bit Parallel \* Driving Method : 1/16Duty, 1/5Bias

\* Viewing Direction : 6 O'clock \* Backlight : 2 LED/White

\*Sample NO. : -

# 2. MECHANICAL SPECIFICATIONS

Item	Specification	Unit
Module Size	122(H) x 44(V) x 13.3 (D) MAX	mm
Viewing Area	99(H) x 25(V)	mm
Activity Display Area	94.84(H) x 20(V)	mm
Character Font	5x8 Dots	-
Character Size	4.84(H) x 9.66(V)	mm
Character Pitch	6(H) x 10.34(V)	mm
Dot Size	0.92(W)x1.1(H)	mm

# 3. ELECTRICAL SPECIFICATIONS

### 3-1 ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C)

Item	Symbol	Min	Max	Unit
Supply Voltage For Logic	Vdd	-0.3	+7	V
Supply Voltage For LCD Drive	$V_{LCD}$	VDD-15.0	VDD+0.3	V
Input Voltage	Vin	-0.3	VDD+0.3	V
Operating Temp.	Тор	-20	+85	°C
Storage Temp.	Tst	-30	+85	°C

<sup>\*.</sup> NOTE: The response time will be extremely slow when the operating temperature is around -10 $^{\circ}$ C, and the back ground will become darker at high temperature operating.

### **3-2 ELECTRICAL CHARACTERISTICS**

It	em	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Logic sup	Logic supply Voltage  LCD Drive			3	3.3	3.6	V
LCE				5.7	6	6.3	V
	"H" Level (Except OSC1)	V <sub>IH1</sub>	Ta = 25 °C VDD=3.3V ± 10%	$0.7V_{DD}$	-	$V_{\scriptscriptstyle DD}$	V
	"L" Level (Except OSC1)	V <sub>IL1</sub>		-0.3	-	0.6	V
Input Voltage	"H" Level (OSC1)	V <sub>IH2</sub>		$V_{\scriptscriptstyle DD}$ -1	ı	$V_{\scriptscriptstyle DD}$	V
	"L" Level (OSC1)	V <sub>IL2</sub>		-0.2	1	1.0	V
Frame Frequency		f <sub>FLM</sub>		-	84.7	1	Hz
Current C	onsumption	I <sub>DD</sub>		-	1.8	-	mA

### 3-3BACKLIGHT

### 3-3-1. Absolute Maximum Ratings

Item	Symbol	Condition	min	Тур	Max	Unit
Forward Current	IF	Ta = 25 °C	-	-	25*2	mA
Power Dissipation	PD		-	-	75*2	mW
Reverse Current	IR	VR=5.0V/LED	ı	1	15	uA

### 3-3-2. Electrical-optical Characteristics

Item	Symbol	Condition	m	in	T	ур	M	ax	Unit
Forward Current	IF			-	15*2		15*2 36		mA
Average Luminous Intensity	Lv	Vf=3.0V Ta = 25 °C	10	00	500		-		cd/m <sup>2</sup>
Colourcoordonate	_		Χ	Υ	Χ	Υ	Х	Υ	
Colourcoordonate			0.25	0.25	0.28	0.28	0.32	0.32	_

The brightness is measured without LCD panel

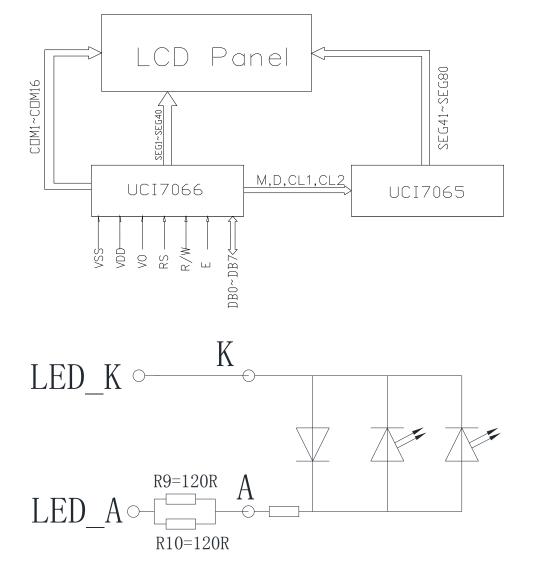
For operation above 25 °C,The lfm&Pd must be derated, the current derating is -0.36mA/°C for DC drive and -0.86mA/°C for Pulse drive, the Power dissipation is -0.75mW/°C.The product working current must not more than the 60% of the lfm or lfp according to the working temperature.

# 4. TERMINAL FUNCTIONS AND BLOCK DIAGRAM

### **4-1INTERFACE PIN FUNCTION DESCRIPTION**

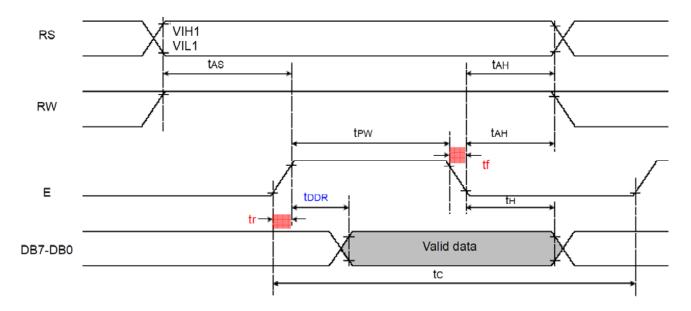
PIN NO.	SYMBOL	FUNCTIONS
1	BLA	Backlight(+5.0V)
2	BLK	Backlight(-)
3	VSS	Ground
4	VDD	Supply voltage for logical circuit
5	V0	Supply voltage for LCD driving
6	RS	A signal for selecting registers.  1: Data Register (for read and write)  0: Instruction Register (for write)
7	R/W	A signal for selecting read or write actions.1: Read, 0: Write.
8	E	A enable signal for reading or writing data.
9-16	DB0~DB7	8 Bit Data Bus

### **4-2 BLOCK DIAGRAM**

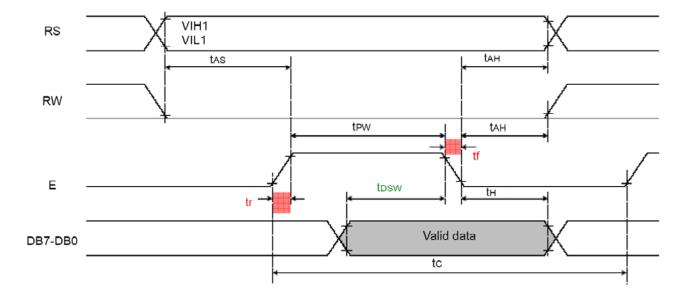


# **5. TIMING CHARACTERISTICS**

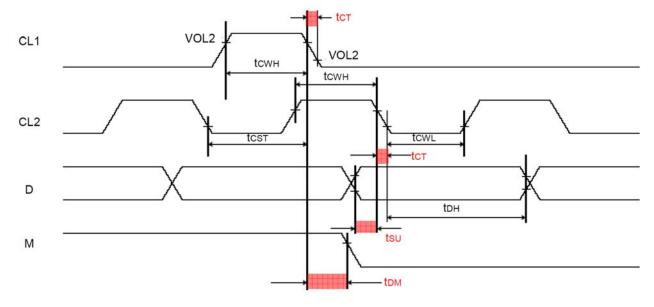
# 5-1 Reading data from UCI7066U to MPU



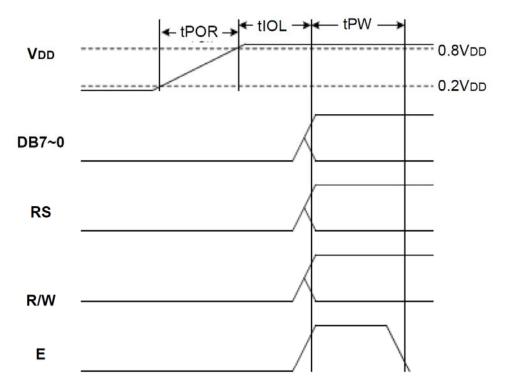
# 5-2 Writing data from MPU to UCI7066U



# 5-3 With External Driver



# **5-4 Power Supply Conditions**



Symbol	Characteristics	Description	Min	Тур.	Max.	Unit
tPOR	Power Rise time	Power rise time that will trigger internal POR circuit	0.1		100	mS
tIOL	I/O Low time	The period that I/O is kept LOW	40			mS
tPW	Enable Pulse width	Please refer to the following tables				

# 5-5 Parameter

Ta=25°C, VDD=5V

Symbol	Characteristic	Test Condition	Min.	Тур.	Max.	Unit
Internal Clock	Operation					
fosc	OSC Frequency	R=91ΚΩ	190	270	350	KHz
External Clock	k Operation		•	•	•	
fEX	External Frequency		125	270	410	KHz
	Duty Cycle		45	50	55	%
tr, tr	Rising/Falling Time				0.2	uS
Write Mode (N	IPU writes data to UCi7066)					
tc	Enable Cycle Time	Pin E	1200			nS
tpw	Enable Pulse Width	Pin E	140			nS
tr, tr	Rising/Falling Time	Pin E			25	nS
tas	Address Setup Time	Pin: RS, RW, E	0			nS
tah	Address Hold Time	Pin: RS, RW, E	10			nS
tosw	Data Setup Time	Pin: DB7~DB0	40			nS
tн	Data Hold Time	Pin: DB7~DB0	10			nS
Read Mode (M	IPU reads data from UCi7066	)		•		•
tc	Enable Cycle Time	Pin E	1200			nS
tpw	Enable Pulse Width	Pin E	140			nS
tr, tr	Rising/Falling Time	Pin E			25	nS
tas	Address Setup Time	Pin: RS, RW, E	0			nS
tah	Address Hold Time	Pin: RS, RW, E	10			nS
todr	Data Setup Time	Pin: DB7~DB0			100	nS
tн	Data Hold Time	Pin: DB7~DB0	10			nS
Interface Mod	e with LCD Driver (UCi7065)					
tcwn	Clock Pulse Width, High	Pin: CL1, CL2	800			nS
tcwL	Clock Pulse Width, Low	Pin: CL1, CL2	800			nS
tcsT	Clock Setup Time	Pin: CL1, CL2	500			nS
tsu	Data Setup Time	Pin: D	300			nS
tрн	Data Hold Time	Pin: D	300			nS
tрм	M Delay Time	Pin: M	0		2000	nS

# **6 COMMAND LIST**

### **6-1 Instruction Table**

The following is a list of host commands supported by UCi7066

R/S: 0: Control, 1: Data W/R: 0: Write Cycle, 1: Read Cycle D7-D0: -: Don't Care

#	Command	RS	R/W	D7	D6	D5	D4	D3	D2	D1	D0	Action
1	Clear Display	0	0	0	0	0	0	0	0	0	1	Clear the screen
2	Return Home	0	0	0	0	0	0	0	0	1		Move cursor to HOME
3	Set Entry Mode	0	0	0	0	0	0	0	1	I/D	s	I/D: Left / Right S: Shift OFF/ON
4	Display ON/OFF	0	0	0	0	0	0	1	D	С	В	D: Display OFF / ON C: Cursor OFF / ON B: Blink OFF / ON
5	Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	-	3.—3.E	S/C: Screen / Cursor R/L Right / Left
6	Set Function	0	0	0	0	1	DL	N	F	-	I	DL: 4-bit / 8-bit, N: 1-line / 2-line F: 5x8 / 5x11
7	Set CGRAM address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	
8	Set DDRAM address	0	0	1	AC12	AC11	AC10	AC9	AC8	AC7	AC6	
9	Read Busy Flag and address	0	1	BF	AC19	AC18	AC17	AC16	AC15	AC14	AC13	
10	Write data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data to RAM
11	Read data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from RAM

### Note:

Ensure that UCi7066 is not in the BUSY state (BF = 0) before sending an instruction from the MPU to the UCi7066. If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself.

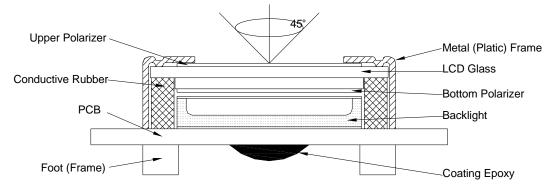
# 7. CHARACTER GENERATOR ROM

No. 7066-03

44			0		.4	4				5 - 3					0. 700	00-03
Upper 4 bits Lower 4 bits	0000	0001	0010	0011	0100	0101	0110	0111	1 000	1 001	1010	1011	11 00	11 01	1110	1111
0000								-						•		
0001												A				
0010													٦.			
0011			Ħ													3.,
0100			\$	4												
0101																
0110							f						<b></b>			
0111			•											I		Ħ
1000			K											I		#
1001			N		I	W								1		
1010			*		J			7			¢			J.		4
1011				7	K		k					.71	7.7			ŧ
1100						đ.	1									H
1101											Ŀ		d.			
1110				*												Ħ
1111													£			

# 8. QUALITY SPECIFICATIONS

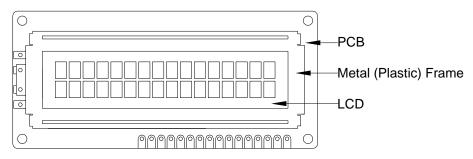
- 8-1. LCM Appearance and Electric inspection Condition
  - 1. Inspection will be done by placing LCM 30cm away from inspector's eyeballs under normal illumination.



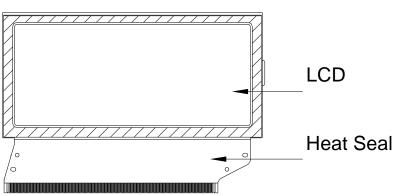
2. View Angle: with in 45° around perpendicular line.

### 8-2. Definition

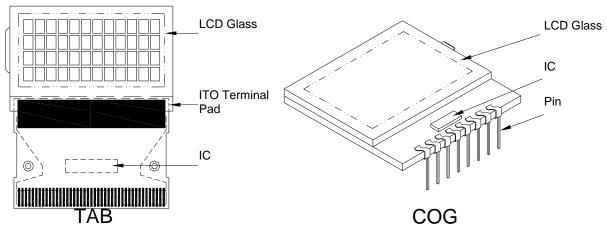
1. COB



2. Heat Seal



3. TAB and COG



# 8-3. Sampling Plan and Acceptance

1. Sampling Plan

MIL - STD - 105E ( || ) ordinary single inspection is used.

2.Acceptance

Major defect: AQL = 0.65%Minor defect: AQL = 1.5%

### 8-4. Criteria

### 1.COB

Defect	Inspection Item	Inspection Standards					
Major	PCB copper flakes peeling off	Any copper flake in viewing Area should be greater than 1.0mm <sup>2</sup>	Reject				
Major	Height of coating epoxy	Exceed the dimension of drawing	Reject				
Major	Void or hole of coating epoxy	Expose bonding wire or IC	Reject				
Major	PCB cutting defect	Exceed the dimension of drawing	Reject				

### 2.SMT

Defect	Inspection Item	Inspection Standa	ards
Minor	Component marking not readable		Reject
Minor	Component height	Exceed the dimension Of drawing	Reject
Major	Component solder defect (missing, extra, wrong component or wrong orientation		Reject
Minor	Component position shift  component soldering pad  X  D  Y  Y	X < 3/4Z Y > 1/3D	Reject Reject
Minor	Component tilt component  soldering pad	Y > 1/3D	Reject
Minor	Insufficient solder component PAD ← PCB	θ <b>≤</b> 20°	Reject

### 3. Metal (Plastic) Frame

Defect	Inspection Item	li	nspection Standa	rds			
Major	Crack / breakage	Any	ywhere	Reject			
		W	L	Acceptable of Scratch			
		w<0.1mm	Any	Ignore			
		0.1 <u>&lt;</u> w<0.2mm	L <u>&lt;</u> 5.0mm	2			
Minor	Frame Scratch	0.2 <u>&lt;</u> w<0.3mm	L <u>&lt;</u> 3.0mm	1			
		w <u>&gt;</u> 0.3mm	Any	0			
		with distance g	e criteria applicable reater than 5mm. on the back sid ignored .				
				Acceptable of Dents / Pricks			
		Φ<	2				
	Frame Dent , Prick	1.0<	1				
Minor	$\Phi = \frac{L + W}{2}$	1.5	0				
	2	Note: 1. Above criteria applicable to any two dents / pricks with distance greater than 5mm 2. Dent / prick on the back side of frame (not visible) can be ignored					
Minor	Frame Deformation	Exceed the dimension of drawing					
Minor	Metal Frame Oxidation		Any rust				

4. Flexible F	1	` '	In an action Oten des				
Defect	insp	ection Item	Inspection Standards				
Minor	Tilted soldering		Tilted soldering Within the angle +5°		Acceptable		
Minor	Uneven solder joint /bump			Reject			
			Expose the conductive line	Reject			
Minor	Hole	$\Phi = \frac{L + W}{2}$	Ф> 1.0mm	Reject			
Minor	Position shift		Y > 1/3D	Reject			
IVIII IOI	**************************************	- <del>-</del>	X > 1/2Z	Reject			

**Model No.:** LT-1602E-605 14/19 Ver:1.1

### 5. Screw

Defect Inspection Item		Inspection Standards	
Major	Screw missing/loosen		Reject
Minor	Screw oxidation	Any rust	Reject
Minor	Screw deformation	Difficult to accept screw driver	Reject

### 6. Heatseal \ TCP \ FPC

Defect	Inspection Item	Inspection Standards	
Major	Scratch expose conductive layer		Reject
Minor	HS Hole $\Phi = \frac{L + W}{2}$	⊕> 0.5mm	Reject
Major	Adhesion strength	Less than the specification	Reject
Minor	Position shift	Y > 1/3D	Reject
IVIII IOI	X	X > 1/2Z	Reject
Major	Conductive line break		Reject

7. LED Backing Protective Film and Others

Defect	Inspection Item	Inspection Standards					
	LED dirty, prick	Acceptable number of units					
		⊕ <u>&lt;</u> 0.10mm	Ignore				
		0.10<⊕ <u>&lt;</u> 0.15mm	2				
Minor		0.15<⊕ <u>&lt;</u> 0.2mm	1				
		Φ>0.2mm	0				
		The distance between any two spots should be ≥ Any spot/dot/void outside of viewing area is accept					
Minor	Protective film tilt	t Not fully cover LCD					
Major	COG coating	Not fully cover ITO circuit	Reject				

8. Electric Inspection

or Electric medication								
Defect	Inspection Item	Inspection Standards						
Major	Short		Reject					
Major	Open		Reject					

# 9. Inspection Specification of LCD

Defect	Insp	ect Item			Ins	spection	St	andards		
		*Glass Scratch	W	,		0.03	0.0	_		V>0.05
		*Polarizer Scratch	L		L۰	<5		L<3		Any
Minor	Linear Defect	* Fiber and Linear	ACC. NO.	1			1		Reject	
		material	Note	L is the length and W is the				e width of	the de	efect
		* Foreign material		Φ <u>&lt;</u> 0.		0.1<⊕ <u>&lt;</u> 0	.15	.15 0.15<⊕ <u>&lt;</u> 0.2		Φ>0.2
	Black Spot and	between glass and polarizer or glass		3EA 100mi	/ m²	2		1		0
Minor	Polarizer Pricked	and glass  * Polarizer hole or protuberance by external force	Note		$\boldsymbol{\Phi}$ is the average diameter of the defect. Distance between two defects >10mm.					
		* Unobvious	_	(	<b>⊅<u>&lt;</u>(</b>	0.3	0.3<	<⊕ <u>&lt;</u> 0.5	0.	5<⊕
	White Spot	transparant foreign material between		3EA	/ 10	00mm <sup>2</sup>		1		0
Minor and Bubble in polarizer		glass and glass or glass and polarizer * Air protuberance between polarizer and glass		$\Phi$ is the average diameter of the defect. Distance between two defects >10mm.						
	Segment Defect		Φ	Φ <u>&lt;</u> 0.1		0.10<⊕ <u>&lt;</u> 0.20		0 0.20<⊕ <u>&lt;</u> 0.25		Φ>0.25
		w the second sec	ACC. NO.	3EA 100mr	/ m²	2		1		0
Minor				W is m	ore	than 1/2 s	egme	nt width		Reject
			Note	Note $\Phi = \frac{L + W}{2}$ Distance between two defect is 10m				m		
			Φ	Φ <u>&lt;</u> 0.1	10	0.10<Φ <u>&lt;</u> 0		<0.20 0.20<⊕<0.2		Φ>0.25
	Protuberant	w W	W	Glue	lue W <u>&lt;</u> 1/2 S W <u>&lt;</u> 0.2				Ignore	
Minor	Segment	$\Phi = (L + W)/2$	ACC. NO.	3EA 100mr	/ m²	2		1		0
			1. Seg	ment						
			E	3	B <u>&lt;</u>	0.4mm	0.4 <e< td=""><td>3<u>&lt;</u>1.0mm</td><td>B&gt;1</td><td>1.0mm</td></e<>	3 <u>&lt;</u> 1.0mm	B>1	1.0mm
Minor	Assembly		B-	·A	B-/	A<1/2B	B-	A<0.2	B-A	<0.25
	Mis-alignment		Judge Acceptable			Acc	Acceptable Acceptable			
			2. Dot	Matrix						1
			Deformation>2° Rej				Reject			
Minor	Stain on LCD Panel Surface		Accept when stains can be wiped lightly with a soft clot or a similar one. Otherwise, judged according to the above items: "Black spot" and "White Spot"							

16/19 **Model No.:** C1602Q8FSW3B-E2 **Ver:**1.1

# 9. RELIABILITY

No	Item	Condition	Quantity	Criteria
1	High Temperature Operating	85°C, 96Hrs	2	GB/T2423.2 -2008
2	Low Temperature Operating	-20℃, 96Hrs	2	GB/T2423.1 -2008
3	High Humidity	60°C, 90%RH, 96Hrs	2	GB/T2423.3 -2006
4	High Temperature Storage	85℃, 96Hrs	2	GB/T2423.2 -2008
5	Low Temperature Storage	ow Temperature Storage -30°C, 96Hrs		GB/T2423.1 -2008
6	Thermal Cycling Test	-20°C, 60min~85°C, 60min, 20 cycles.	2	GB/T2423.2 2 -2012
7	Packing vibration	Frequency range:10Hz~50Hz Acceleration of gravity:5G X,Y,Z 30 min for each direction.	2	GB/T5170.1 4 -2009
8	Electrical Static Discharge	Air: $\pm$ 8KV 150pF/330 $\Omega$ 5 times	2	GB/T17626.
	2	Contact: $\pm 4$ KV 150pF/330 $\Omega$ 5 times		-2006
9	Drop Test (Packaged)	Height: 80 cm, 1 corner, 3 edges, 6 surfaces.	2	GB/T2423.8 -1995

Note:1) Above conditions are suitable for our company standard products.

<sup>2)</sup> For restrict products, the test conditions listed as above must be revised.

### 10. HANDLING PRECAUTION

### (1) Mounting Method

The panel of the LCD Module consists of two thin glass plates with polarizers which easily getdamaged since the Module is fixed by utilizing fitting holesin the printed circuit board. Extreme care should be taken when handling the LCD Modules.

(2) Caution of LCD handling & cleaning

When cleaning the display surface, use soft cloth with solvent (recommended below) and wipe lightly.

- Isopropyl alcohol
- Ethyl alcohol
- Trichlorotrifloro thane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Ketone
- Aromatics

### (3) Caution against static charge

The LCD Module use C-MOS LSI drivers, so we recommend that you connect any unused input terminal to VDD or VSS, do not input any signals before power is turned on. Andground your body, Work/assembly table. Andassembly equipment toprotect against staticelectricity.

### (4) Packaging

- Modules use LCD elements, and must be treated as such. Avoid intense shock and falls from a height.
- To prevent modules from degradation. Do not operate or store them exposed directly to sunshine or high temperature/humidity.

### (5) Caution for operation

- It is indispensable to drive LCD's within the specified voltage limit since the higher voltage than the limit shorten LCD life. An electrochemical reaction due to direct current causes LCD deterioration, Avoid the use of direct current drive.
- Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's. Which will come back in the specified operating temperature range.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the relative condition of 40°C, 50%RH or less is required.

### (6) Storage

In the case of storing for a long period of time (for instance.) For years) for the purpose or replacement use, The following ways are recommended.

- Storage in a polyethylene bag with sealed so as not to enter fresh air outside in it, And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light is. Keeping temperature in the specified storage temperature range.
- Storing with no touch on polarizer surface by the anything else. (It is recommended to store them as they have been contained in the inner container at the time of delivery)

### (7) Safety

- It is recommendable to crash damaged or unnecessary LCD into pieces and wash off liquid crystal by using solvents such as acetone and ethanol. Which should be burned up later.

When any liquid crystal leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

### (8) Other

- After the product shipped, any product quality issues must be feedback within three months, otherwise, we will not be responsible for the subsequent or consequential events.

# 11. OUTLINE DIMENSION

NOTE: The dimension with "()" is reference

